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\*\* CONTINUING DATA \*\*\*\*\* *YES* \*\*\*\*\*  
 This application is a 371 of PCT/JP03/06428 05/22/2003

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *YES* \*\*\*\*\*  
 JAPAN 2002-148129 05/22/2002  
 JAPAN 2002-262793 09/09/2002  
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 31	TOTAL CLAIMS 44	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

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## TITLE

Low-pass filter for a pll, phase-locked loop and semiconductor integrated circuit